

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|--------|--|--------------------|------------------|---------|------------------|
| L1 | 8464 | steer\$3 and control and word | US-PGPUB; USPAT | OR | OFF | 2006/12/28 14:20 |
| L2 | 355 | l1 and crossbar | US-PGPUB; USPAT | OR | OFF | 2006/12/28 14:20 |
| L3 | 128189 | l2 amd multiplexer | US-PGPUB; USPAT | OR | OFF | 2006/12/28 14:20 |
| L4 | 191 | l2 and multiplexer | US-PGPUB; USPAT | OR | OFF | 2006/12/28 14:20 |
| L5 | 122 | l4 and @ad<"20011109" | US-PGPUB; USPAT | OR | OFF | 2006/12/28 14:21 |
| S1 | 1 | ("20030088826").PN. | US-PGPUB; USPAT | OR | OFF | 2006/12/28 07:33 |
| S2 | 49 | ("4764925" "4903150" "5361363" "5872987" "6141313" "6219775" "6219775" "4357658" "4443850" "5333268" "5353412" "5388214" "5390298" "5513366" "5530809" "5537535" "5590283" "5680550" "5758176" "5805920" "5825749" "6239628" "4437711" "4527285" "4763304" "4780599" "4811214" "4866604" "4878203" "4922413" "5195052" "5204866" "5265207" "5398331" "5423051" "5452466" "5475860" "5481743" "5557545" "5564056" "5574928" "5574942" "5583754" "5612640" "5615385" "5623685" "5712972" "5778247" "5778210" "5793946").pn. | US-PGPUB; USPAT | OR | OFF | 2006/12/28 07:34 |
| S3 | 379 | (712/220).ccls. | US-PGPUB; USPAT | OR | OFF | 2006/12/28 08:01 |
| S4 | 2857 | (709/238).ccls. | US-PGPUB; USPAT | OR | OFF | 2006/12/28 07:52 |
| S5 | 0 | (715/501).ccls. | US-PGPUB; USPAT | OR | OFF | 2006/12/28 07:37 |
| S6 | 1429 | (715/501.1).ccls. | US-PGPUB; USPAT | OR | OFF | 2006/12/28 09:51 |
| S7 | 2 | ("5930492" "4811214").pn. | US-PGPUB; USPAT | OR | OFF | 2006/12/28 07:53 |
| S8 | 1 | ("5907485").PN. | US-PGPUB; USPAT | OR | OFF | 2006/12/28 09:52 |
| S9 | 738 | (710/316).ccls. | US-PGPUB; USPAT | OR | OFF | 2006/12/28 11:50 |
| S10 | 292 | (710/317).ccls. | US-PGPUB; USPAT | OR | OFF | 2006/12/28 14:19 |



crossbar control word functional unit

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All Results[M Gokhale](#)[J Hauser](#)[J Arnold](#)[R Hartenstein](#)[W Dally](#)[The NAPA adaptive processing architecture - group of 7 »](#)

CR Rupp, M Landguth, T Garverick, E Gomersall, H ... - FPGAs for Custom Computing Machines, 1998. Proceedings. IEEE ..., 1998 - [ieeexplore.ieee.org](#)

... Although not as flexible as a **crossbar** style interconnect structure, the ToggleBus nevertheless implements all of the major ... Pipeline **Control Ports** ... **Status Word** ...

[Cited by 112](#) - [Related Articles](#) - [Web Search](#)

[A low-power, high-performance, 1024-point FFT processor - group of 6 »](#)

BM Baas - Solid-State Circuits, IEEE Journal of, 1999 - [ieeexplore.ieee.org](#)

... and from (1), the cache size equals **words**. ... the chip also contains **crossbar**, **control**, clock generation ... a brief overview of each **functional unit** type, followed by ...

[Cited by 72](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Multiprocessor coupling system with integrated compile and run time scheduling for parallelism - group of 3 »](#)

SW Keckler, WJ Dally - US Patent 5,574,939, 1996 - [Google Patents](#)

... Timeout Limit Register, and a **control word** containing HALT ... Finally, the Global **Control**

Unit sequen- tializes the ... C-Switch) 24 is a **crossbar** switch with four buses ...

[Cited by 65](#) - [Related Articles](#) - [Web Search](#)

[Wormhole run-time reconfiguration - group of 14 »](#)

R Bittner, P Athanas - Proceedings of the 1997 ACM fifth international symposium on ..., 1997 - [portal.acm.org](#)

... used to inject the exponent and mantissa **words** of the ... the new input to gain exclusive **control** of the ... small and reside compactly underneath the **crossbar** routing ...

[Cited by 44](#) - [Related Articles](#) - [Web Search](#)

[\[PS\] Architecture Design of Reconfigurable Pipelined Datapaths - group of 7 »](#)

DC Cronquist, P Franklin, C Fisher, M Figueroa, C ... - Proceedings of the 20th Anniversary Conference on Advanced ..., 1999 - [citeseer.csail.mit.edu](#)

... Unfortunately, the chip area of the **crossbar** scales with ... The buses are **word**-based instead of bit based ... simplifies the layout and reduces the **control** requirements ...

[Cited by 75](#) - [Related Articles](#) - [View as HTML](#) - [Web Search](#)

[A generic architecture for on-chip packet-switched interconnections - group of 21 »](#)

P Guerrier, A Greiner - Proceedings of the conference on Design, automation and test ..., 2000 - [portal.acm.org](#)

... The regular parts (buffers and **crossbar**) were placed manually ... Figure 7: Router floorplan

The **control** logic can ... network (16 routers) with 20-**word** packets spread ...

[Cited by 249](#) - [Related Articles](#) - [Web Search](#)

[Garp: A MIPS Processor with a Reconfigurable Coprocessor - group of 24 »](#)

JR Hauser, J Wawrzynek - IEEE Symposium on FPGAs for Custom Computing Machines, 1997 - [doi.ieeeecs.org](#)

... row serve as liaisons between the array and the outside world. Among other things, **control** blocks can interrupt the main ... **crossbar crossbar crossbar crossbar** ...

[Cited by 474](#) - [Related Articles](#) - [Web Search](#)

[Colt: An Experiment in Wormhole Run-time Reconfiguration - group of 12 »](#)
R Bittner, P Athanas, M Musgrove - SPIE Photonics East96, Boston, MA, USA, Nov, 1996 - 128.173.52.3

... receive their northern local inputs and skip bus connections from the **crossbar**. ... Outputs To **Cross Bar** ... It consists of the FU surrounded by the **control** and buses ...

[Cited by 23](#) - [Related Articles](#) - [View as HTML](#) - [Web Search](#)

[Logical, fail-functional, dual central processor units formed from three processor units - group of 2 »](#)

RW Horst - US Patent 5,838,894, 1998 - Google Patents

... The system achieves a low cost fail-**functional** architecture. 7Claims, 30 Drawing Sheets Page 2. ... FIG. 16. INTERNAL CONTROL ERROR ERROR ... I EVEN WORDS ODD WORDS 28 ...

[Cited by 30](#) - [Related Articles](#) - [Web Search](#)

[Dynamic codewidth reduction for VLIW instruction set architectures in digital signal processors](#)

M Weiss, GP Fettweis - 3rd. Int. Workshop in Signal and Image Processing (IWSIP ' ..., 1996 - ifn.et.tu-dresden.de

... kind can be used to simplify the **crossbar** switch ... the TVLIW scheme on a real-world-example, we ... distinct FUs are required: a program **control unit** (PCU) including ...

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